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Distributed Synthesis for LTL Fragments

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Abstract—We consider the distributed synthesis problem for temporal logic specifications. Traditionally, the problem has been studied for LTL, and the previous results show that the problem is decidable iff there is no information fork in the architecture. We consider the problem for fragments of LTL and our main results are as follows: (1) We show that the problem is undecidable for architectures with information forks even for the fragment of LTL with temporal operators restricted to next and eventually. (2) For specifications restricted to globally along with non-nested next operators, we establish decidability (in EXPSAPCE) for star architectures where the processes receive disjoint inputs, whereas we establish undecidability for architectures containing an information fork-meet structure. (3) Finally, we consider LTL without the next operator; and establish decidability (NEXPTIME-complete) for all architectures for a fragment that consists of a set of safety assumptions, and a set of guarantees where each guarantee is a safety, reachability, or liveness condition.

I. INTRODUCTION

Synthesis and distributed synthesis. The synthesis problem is the most rigorous form of systems design, where the goal is to construct a system from a given temporal logic specification. The problem was originally proposed by Church [1] for synthesis of circuits, and has been revisited in many different contexts, such as supervisory control of discrete event systems [2], synthesis of reactive modules [3], and several others. In a seminal work, Pnueli and Rosner [4] extended the classical synthesis problem to a distributed setting. In the distributed synthesis problem, the input consists of (i) an architecture of synchronously communicating processes, that exchange messages through communication channels; and (ii) a specification given as a temporal logic formula, and the synthesis question asks for a reactive system for each process such that the specification is satisfied. The most common logic to express the temporal logic specification is the linear-time temporal logic (LTL) [5].

Previous results for distributed synthesis for LTL. In general the distributed synthesis problem is undecidable for LTL, but the problem is decidable for pipeline architectures [4]. The undecidability proof uses ideas originating from the undecidability proof of three-player imperfect-information games [6], [7]. The decidability results for distributed synthesis were extended to other similar architectures, such as one-way rings [8], and also a distributed games framework was proposed in [9]. Finally, a complete topological criterion on the architecture for decidability of distributed synthesis for LTL was presented [10], where it was shown that the problem is decidable if and only if there is no information fork in the underlying architecture. Architectures without information forks can essentially be reduced to pipelines.

Fragments of LTL. While LTL provides a very rich framework to express temporal logic specifications, in recent years, several fragments of LTL have been considered for efficient synthesis of systems in the non-distributed setting. Such fragments often encompass a large class of properties that arise in practice and admit efficient synthesis algorithms, as compared to the whole LTL. In [11], [12] the authors considered a fragment of LTL with only eventually (reachability) and globally (safety) as the temporal operators. In [13] LTL with only eventually and globally operators (but without next and until operators) was considered for efficient translation to deterministic automata. The temporal logic specifications for reactive systems often consist of a set of assumptions and a set of guarantees, and the reactive system must satisfy the guarantees if the environment satisfies the assumptions. In [14] the GR1 (generalized reactivity 1) fragment of LTL was introduced where each assumption and guarantee is a liveness condition; and it has been shown that GR1 synthesis is very effective to automatically synthesize industrial protocols such as the AMBA protocol [15], [16].

Our contributions. In this work we consider the distributed synthesis problem for fragments of LTL. The previous results in literature considered the whole LTL and characterized architectures that lead to decidability of distributed synthesis. In contrast, we consider fragments of LTL to present finer characterizations of the decidability results. Our main contributions are as follows:

1) Reachability properties. First we consider the fragment of LTL with next and eventually (reachability) as the only temporal operators, and establish that the distributed synthesis problem is undecidable if there is an information fork in the underlying architecture. In particular, the problem is undecidable with one nesting depth of the next operator and only one eventually operator; i.e., if we consider the fragment of LTL that consists of Boolean combinations of atomic propositions and next of atomic propositions; and only one eventually as the temporal operator, then the distributed synthesis problem is undecidable iff there is an information fork in the architecture.

2) Safety properties. We then consider the fragment of LTL with next and globally (safety) as the only temporal operators, with a single occurrence of the globally operator. We show that the distributed synthesis problem can be decidable under the existence of information forks; in particular we establish decidability (in EXPSAPCE) for the star architecture where processes have no common inputs from the environment. However, we show that
the problem remains undecidable for architectures containing an information fork-meet, a structure in which two processes receive sets of disjoint inputs, (as in the information fork case), and a third process receives the union of those sets. Moreover, our undecidability proof again uses specifications that do not contain nested next operators. In other words, if there is information fork, the problem may be decidable, but if there is information fork, and then the forked information meets again, then we obtain undecidability.

3) Temporal specifications without the next operator. Since our results show that even with one nesting depth of the next operator, distributed synthesis is undecidable with reachability and safety objectives, we finally consider the problem without the next operator. We show that if we consider a set of safety assumptions, and a set of guarantees such that each guarantee is a safety, reachability, or a liveness guarantee, then the distributed synthesis problem is decidable (and NEXPTIME-complete) for all architectures.

Hence, our paper improves upon existing results by presenting finer (un)decidability characterizations of the distributed synthesis problem for fragments of LTL. We also remark that when we establish decidability, it is either EXPSPACE or NEXPTIME-complete, as compared to previous proofs of decidability in distributed synthesis setting where the complexity is non-elementary. Thus as compared to the complexity of previous decidability results (tower of exponentials), our complexity (at most two exponentials) are very modest.

II. MODEL DESCRIPTION

Architectures. An architecture is a tuple \( A = (P, P_e, V, E) \), where \( P = \{p_1, p_2, \ldots, p_n\} \) is a set of \( n + 1 \) processes, \( P_e \) is a distinguished process representing the environment, \( V \) is a set of (output) binary variables, and \( E : P \times P \to 2^V \) defines the communication variables between processes (i.e., \( E(p, q) = \{u, v\} \) means that \( p \) writes to variables \( u, v \), and \( q \) reads from them). For every process \( p \in P \), we denote with \( O(p) = \bigcup_{q \in P} E(p, q) \) the set of output variables of \( p \), and with \( I(p) = \bigcup_{q \in P} E(q, p) \) the set of input variables of \( p \). We require that for all \( p, q \in P \), \( O(p) \cap O(q) = \emptyset \), i.e., no two processes write to the same variable. Finally, we will denote with \( P^* = P \setminus \{p_e\} \).

An architecture describes a distributed reactive system, with the environment providing the inputs via \( O(p_e) \), and the system responding via \( I(p_e) \). The pair \( (P, E) \) describes the architecture of the system as a multigraph, with \( P \) being the set of nodes, and \( E(p, q) \) the set of directed \( p \to q \) edges with the corresponding variables as labels.

Trees. We define a (full) \( B \)-tree \( T \) over some finite set \( B \) as the set of all nodes \( x \in (2^B)^* \). A (possibly infinite) sequence of nodes \( \pi = (x_1, x_2, \ldots) \) forms a path in \( T \), if for every \( i \geq 1 \) we have \( x_{i+1} = x_i z \), for some \( z \in 2^B \). For such a path \( \pi \), we will use \( \pi[i] \) to denote the element of \( \pi \) in the \( i \)-th position, while \( \pi[i, \infty) \) denotes the infinite suffix of \( \pi \) starting at position \( i \). An \( A \)-labeled \( B \)-tree \( T_A \) is a \( B \)-tree equipped with a labeling function of its nodes, \( \lambda : (2^B)^* \to 2^A \). For every node \( x = yz \in T_A \) with \( z \in 2^B \) we denote with \( \ell_A(x) = z \cup \lambda(x) \), i.e., the \( \ell_A \) of \( x \) consists of the branch \( z \) from the parent and the label \( \lambda \) of \( x \). For a (possibly infinite) path \( \pi = (x_1, x_2, \ldots) \), we define with \( \ell_A(\pi) = (\ell_A(x_1), \ell_A(x_2), \ldots) \).

Local strategies. For every process \( p \in P^- \), a local strategy \( \sigma_p \) is a function \( \sigma_p : (2^P)^* \to 2^{O(p)} \), setting the output variables of \( p \) according to the history of its input variables. A local strategy \( \sigma_p \) has finite memory if there exists a finite set \( M, m_0 \in M \), and functions \( f : M \times 2^f \to M \) and \( g : M \to 2^{O(p)} \) such that for all \( x = x_1x_2\ldots x_k \) with \( x_i \in 2^f \), we have \( \sigma_p(x) = g(f(\ldots (f(f(m_0, x_1), x_2), \ldots), x_k)) \).

The memory of \( \sigma_p \) is said to be \([M]\), while if \([M] = 1\), then \( \sigma_p \) is called memoryless.

Collective strategies. Every such local strategy \( \sigma_p \) can be viewed as a labeling of an \((O(p))\)-labeled \( I(p)\)-tree \( T_{\sigma_p} \). The \( \text{collective strategy of the architecture } A \) is a function \( \sigma : (2^P)^* \to 2^{V \setminus O(p_e)} \), mapping every finite sequence of the outputs of the environment to a subset of the outputs of the processes \( p \) according to the composition \( (\sigma_p : p \in P^-) \). The collective strategy \( \sigma \) can be viewed as a \((V \setminus O(p_e))\)-labeled \( O(p_e)\)-tree \( T_\sigma \) and for any infinite path \( \pi \) in \( T_\sigma \), we will call \( \ell_\sigma(\pi) \) a computation. Hence, \( T_\sigma \) describes a distributed algorithm, and every infinite path \( \pi = (x_1, x_2, \ldots) \) starting from the root represents a distributed computation \( \ell_\sigma(\pi) \), according to the local strategies \( (\sigma_p : p \in P^-) \).

Synthesis (realizability). We will consider distributed reactive systems with specifications given by temporal logic formulas. For temporal logic formulas we will consider LTL; see [5] for the formal syntax and semantics of LTL. The problem of realizability of a temporal logic formula \( \phi \) in an architecture \( A \) asks whether there exist local strategies \( \sigma_p \) for every process \( p \), such that for every infinite path \( \pi \) of the \((V \setminus O(p_e))\)-labeled \( O(p_e)\)-tree \( T_\sigma \) of the collective strategy \( \sigma \), with \( \pi \) starting from the root, we have \( \ell_\sigma(\pi) \models \phi \). If \( \phi \) admits such strategies \( \sigma_p \) for every \( p \in P^- \), then it is called realizable, and the collective strategy \( \sigma \) gives an implementation for \( \phi \) in \( A \).

III. SYNTHESIS FOR REALIZABILITY SPECIFICATIONS

In the current section we discuss the synthesis problem for realizability specifications, where the objective consists of propositional formulas connected with Boolean operators and non-nested \( X \) (next) operators. We will show that even under such restrictions, the synthesis problem remains undecidable for all architectures containing an information fork, via a reduction from the halting problem of Turing machines.

Fragment LTL\( _\Diamond \). We consider LTL\( _\Diamond \) that consists of formulas \( \phi \) from the following LTL fragment:

\[ \theta = P | X P \]
\[ \psi = \theta_1 \land \theta_2 | \theta_1 \lor \theta_2 | \neg \theta \]
\[ \phi = Q \rightarrow \Diamond \psi \]

where \( P, Q \) are propositional formulas, \( X \) is the next operator, \( \Diamond \) is the eventually temporal operator. We consider the standard semantics of LTL. Formula \( \Diamond \psi \) represents a realizability objective, and \( Q \) will capture the initial input in the architecture.

Turing machines. Let \( M \) be a deterministic Turing machine fixed throughout this section and let \( Q \) be the set of states
of $M$ (see [17] for detailed descriptions of Turing machines). The machine $M$ works over the alphabet $\{0, 1, \sqcup\}$, and its tape is bounded by $\#$ symbols. The machine $M$ cannot move left on a $\#$ symbol, and moving right to a $\#$ symbol effects in extending the tape by a blank symbol $\sqcup$. In our analysis, $M$ starts with the empty tape. A configuration of $M$ is a word $a v q u a w \sqcup \#$, where $a \in \{0, 1\}$, $v, u \in \{0, 1\}^*$ and $q \in Q$. Such a configuration has the standard interpretation as an infinite tape such that $v$ is the part of the tape preceding the head, $q$ is the current state of $M$, $a$ is the letter under the head, and $u$ is a sequence of symbols succeeding the head. The blank symbol $\sqcup$ represents the rightmost cell of the tape that has not been altered by $M$. We define the projection $\pi_\sqcup$ over words $w$ from some alphabet containing $\sqcup$, such that $\pi_\sqcup(w)$ is the result of omitting all $\sqcup$ symbols from $w$. We define a scattered configuration $C$ of $M$ as a word over $\Sigma = \{0, 1, \sqcup, \sqcup, \#$} \cup Q$ such that $\pi_\sqcup(C)$ is a configuration of $M$.

Information-fork architecture. We first consider the architecture $A_0$ (Figure 1), characterized as an information fork in [10], for which the problem of realizability has been shown to be undecidable, using LTL formulae with nested until operators (in [4]). Here we show that the problem remains undecidable for $A_0$ and specifications in the restricted fragment of LTL$_{\square}$. This is obtained through a reduction from the halting problem of $M$, by constructing a specification $\phi \in$ LTL$_{\square}$ which is realizable iff $M$ halts on the empty input.

Proof idea. The architecture $A_0$ consists of the environment $p_e$ and two processes $p_1$ and $p_2$. The processes act as I/O streams, outputting configurations of $M$; the environment sends separately to each process next and stall signals, indicating that the corresponding process should output the next letter from $\{0, 1, \sqcup, \#$} \cup Q$ of the current configuration of $M$, or it should output $\sqcup$.

Construction of $\varphi$. First, we will provide a regular safety property $\varphi$ which specifies that if the environment satisfies an alternation assumption, i.e., every stall signal is followed by a next signal, then $p_1$ and $p_2$ conform with a series of guarantees. The property $\varphi$ does not belong to the LTL$_{\square}$ fragment, but we will show how it can be expressed by a safety automaton $A_{safe}$. Then, we will prove that if $\varphi$ is realizable, and the environment conforms with the alternation assumption, then the processes output a legal sequence of configurations of $M$, scattered with the $\sqcup$ symbol.

Conversion to LTL$_{\square}$. Next, we will provide the specification for the synthesis problem $\phi \in$ LTL$_{\square}$, such that $\phi$ is realizable iff $\varphi$ is realizable and $M$ halts on the empty input. Formula $\phi$ does not express $\varphi$ directly, but it asserts that the environment simulates a run of $A_{safe}$ faithfully, and finally one of the processes outputs a halting configuration of $M$. More precisely, the environment simulates a run of $A_{safe}$ storing the current state of $A_{safe}$ in a set of hidden variables $\{q_1, \ldots, q_m\} \in E(p_e, p_e)$, and $\phi$ encodes that eventually either (i) the environment cheats in the simulation of $A_{safe}$, or (ii) one of the processes outputs a halting state $q$ of $M$, while the current state of $A_{safe}$ is not rejecting (i.e., $q$ was reached legally with respect to $M$). We will conclude that $\phi$ is realizable iff $M$ halts on the empty input.

Fig. 1: The architecture $A_0$ which consists an information fork.

Formal proof. A safety automaton cannot express a scattered configuration that is finite. Thus, we define a scattered preconfiguration $C$ (of $M$) as a (possibly infinite) word whose every finite prefix can be extended to a scattered configuration of $M$. A scattered preconfiguration is formally defined as a finite or infinite word over $\Sigma$ that begins with $\#$, there is at most one symbol from $Q$, there are no symbols after the second $\#$ and the $\sqcup$ symbol is followed by the $\#$ symbol.

Let $C_1, C_2$ be scattered preconfigurations. We denote with $\sqcup(C)$ the set of positions in $C$ where $\sqcup$ occurs, and write $C_1 \sqcup C_2$ if the symmetric difference of $\sqcup(C_1)$ and $\sqcup(C_2)$ has at most one element, i.e., $|\sqcup(C_1)\sqcup \sqcup(C_2)| \leq 1$. We define as $C_1 \sqcup C_2$ if $C_1 \sqcup C_2$ and

(i) $\pi_\sqcup(C_2)$ follows legally from $\pi_\sqcup(C_1)$ according to $M$, or

(ii) both $C_1, C_2$ are infinite preconfigurations such that every finite prefix can be extended to finite preconfigurations $C_1', C_2'$ such that $\pi_\sqcup(C_2')$ follows legally from $\pi_\sqcup(C_1')$.

For infinite words $w_1, w_2$, we define $w_1 \sqcup w_2$ as a word over $\Sigma \times \Sigma$ such that the $i$-th letter of $w_1 \sqcup w_2$ is a pair of the $i$-th letters of $w_1, w_2$. Observe that there are safety automata working over $\Sigma \times \Sigma$ that recognize the languages $\{C_1 \sqcup C_2 : C_1 \sqcup C_2\}$ and $\{C_1 \sqcup C_2 : C_1 \sqcup C_2\}$.

Construction of $\varphi$. We first construct the regular safety property $\varphi = L \rightarrow \bigwedge_{0 \leq i \leq 4} Cond_i$, where $L$ (the alternation assumption) and $Cond_i$ are defined as follows:

$L$: for every process, every stall signal is followed by a next signal.

Cond$_0$: each process outputs $\sqcup$ when its input is stall, otherwise it outputs a letter from $\Sigma \setminus \{\sqcup\}$.

Cond$_1$: each process produces a sequence of scattered preconfigurations,

Cond$_2$: initially, each process produces two scattered configurations of $M$, whose projections are the first two valid configurations of $M$,

Cond$_3$: if starting from some position $i$, $p_1$ outputs consecutively $C_1, C_2$ and $p_2$ outputs consecutively $C_1', C_2'$, then $C_1 \parallel C_1$ implies $C_2 \parallel C_2'$ or $C_2' \parallel C_2$.

Cond$_4$: if $D, D'$ are outputs of $p_1, p_2$ up to some positions such that $D \parallel D'$ and $|\pi_\sqcup(D)| = |\pi_\sqcup(D')|$, then $\pi_\sqcup(D) = \pi_\sqcup(D')$.

We provide a high-level description of the construction of an alternating safety automaton $A_{safe}$ (see [18] for the definition of alternating automata) which verifies that every execution satisfies $\varphi$. Note that $A_{safe}$ can be transformed to a non-deterministic automaton by a standard power-set construction. Clearly, conditions $L, Cond_0$ and $Cond_1$ can be expressed
by a safety automaton. For the condition \( \text{Cond}_2 \), observe that the first two configurations of \( M \) have at most 9 letters \( #q_0 \sqcup \#q_1a \sqcup \# \), with \( a \in \{0, 1, \epsilon\} \). To show that the rest of conditions can be expressed by a safety automaton, we assume that \( \mathcal{L} \) is satisfied; otherwise those conditions do not have to be checked (note that if \( \mathcal{L} \) is violated, \( A_{\text{safe}} \) accepts unconditionally). Because of \( \mathcal{L}, A_{\text{safe}} \) can verify that \( p_1 \) and \( p_2 \) conform with \( \text{Cond}_2 \) by checking the first 18 output letters. For the condition \( \text{Cond}_3, A_{\text{safe}} \) operates as follows: whenever it encounters a \( \# \) symbol marking the beginning of a configuration, it splits universally. One copy looks for the next configuration, and the second copy, denoted by \( A_3 \), verifies that \( \text{Cond}_3 \) holds at the current position, as follows. It ignores \( \bot \) symbols and compares whether \( C_1 \parallel C_1' \), configurations \( \pi_\bot(C_1) \) and \( \pi_\bot(C_1') \) are equal everywhere except for positions adjacent to the head of \( M \), and the letters adjacent to the head are consistent with the transition of \( M \). If one of these conditions is violated, \( C_1' \not\parallel C_1 \), therefore \( A_3 \) accepts the word regardless of what follows. Otherwise, if those conditions hold, i.e., \( C_1' \parallel C_1, A_3 \) non-deterministically verifies one of the following conditions: \( C_2' \not\parallel C_2 \) or \( C_2' \not\parallel C_2 \). Both conditions can be verified by safety automata, since \( C_2 \) and \( C_2' \) either start concurrently, or \( C_2 \) is delayed by 1 step from \( C_2' \). For the condition \( \text{Cond}_4 \) observe that if \( D \sqcup D' \) and \( |\pi_\bot(D)| = |\pi_\bot(D')| \), then \( |D| - |D'| \leq 1 \) and the automaton needs to remember at most one symbol to compare \( \pi_\bot(D) \) and \( \pi_\bot(D') \). We can now prove the following lemma.

**Lemma 1.** If \( \varphi \) is realizable, then for every \( k \in \mathbb{N} \), in all executions where \( \mathcal{L} \) holds, both \( p_1 \) and \( p_2 \) output sequences of scattered configurations whose \( \pi_\bot \) projections are sequences of at least \( k \) consecutive valid configurations of \( M \), starting with the initial configuration on the empty input.

**Proof:** First note that there exist executions where the environment indeed satisfies \( \mathcal{L} \), and thus \( p_1 \) and \( p_2 \) satisfy conditions \( \text{Cond}_0 \sim \text{Cond}_2 \). The lemma clearly holds for \( k = 1, 2 \), due to conditions \( \text{Cond}_0 \sim \text{Cond}_2 \). For the inductive step, assume that the lemma holds for \( k \geq 2 \). Consider a sequence of inputs to \( p_1 \) consisting of \( \text{next} \) signals only. Then, there is a sequence of inputs to \( p_2 \) consisting of some number of \( \text{next} \) signals and exactly \( |\pi_\bot(C_k)| \) signals \( D \) in such a way that \( p_1 \) outputs \( C_1 \ldots C_kC_{k+1} \), \( p_2 \) outputs \( C_1' \ldots C_{k-1}'C_k' \), and \( C_kC_{k+1}, C_k' \) are synchronized, i.e. they start at the same position and \( C_k \parallel C_k' \). By the induction assumption \( \pi_\bot(C_k') \) and \( \pi_\bot(C_k) \) are, respectively, \( (k-1) \)-th and \( k \)-th configurations of \( M \). Therefore, \( C_{k-1} \parallel C_k \) and, by \( \text{Cond}_3, C_k' \parallel C_{k+1} \). This implies that \( C_{k+1} \) is a finite scattered preconfiguration and \( \pi_\bot(C_{k+1}) \) is the \( (k+1) \)-th configuration of \( M \).

Given that for an input consisting of \( \text{next} \) signals only, \( p_1 \) outputs \( C_1 \ldots C_kC_{k+1} \) satisfying the statement, we can show that regardless of the number of \( \text{stall} \) signals, under condition \( \mathcal{L} \), \( p_1, p_2 \) output \( k+1 \) scattered configurations satisfying the statement. First, the condition \( \text{Cond}_4 \) implies that if \( p_2 \) also has an input sequence consisting of \( \text{next} \) signals alone, it will output the same sequence, that is, \( C_1 \ldots C_kC_{k+1} \). By a simple induction on the number of \( \text{stall} \) signals each process receives, and condition \( \text{Cond}_4 \), we conclude that for any number of \( \text{stall} \) signals, as long as \( \mathcal{L} \) is satisfied by the environment, \( p_1, p_2 \) output \( k+1 \) scattered configurations whose projections are the first \( k+1 \) consecutive configurations of \( M \).

**Conversion to \( \text{LTL}_\mathcal{O} \).** Given the safety automaton \( A_{\text{safe}} \) which verifies that \( \varphi \) is satisfied, we can construct a specification \( \phi \in \text{LTL}_\mathcal{O} \), such that \( \phi \) is realizable if and only if the Turing machine \( M \) does not halt on the empty input. The environment uses the hidden (not visible to \( p_1, p_2 \)) variables \( q_1, \ldots, q_k \in \mathbb{P}(p_e, p_e) \) to simulate the automaton \( A_{\text{safe}} \). We provide a high level description of the following formulas:

\( Q \) specifies that the first state of \( A_{\text{safe}} \) according to the output variables \( \{q_1, \ldots, q_m\} \) is compatible with the initial values of \( x_1, x_2, y_1 \) and \( y_2 \) (i.e., \( \{q_1, \ldots, q_m\} \) represent the state of \( A_{\text{safe}} \) reached from the initial state after reading the initial values of \( x_1, x_2, y_1 \) and \( y_2 \); \( Q \) is propositional) \( \psi_1 \) specifies that \( A_{\text{safe}} \) has a transition from the current state to the next state, encoded by the values of \( \{q_1, \ldots, q_m\} \) in the current and the next round, according to the value of variables \( x_1, x_2, y_1 \) and \( y_2 \) in the next round (i.e., \( p_e \) simulates \( A_{\text{safe}} \) faithfully; \( \psi_1 \) contains only propositional and non-nested \( \chi \) operators).

\( \psi_2 \) specifies that the current state of \( A_{\text{safe}} \) is not rejecting, and \( p_1 \) or \( p_2 \) output a halting state of \( M \) (i.e., a halting configuration of \( M \) was reached by some process, and both processes behaved according to \( A_{\text{safe}}; \psi_2 \) is propositional).

Finally, we construct \( \phi = Q \rightarrow \Diamond(\neg \psi_1 \lor \psi_2) \), with \( \phi \in \text{LTL}_\mathcal{O} \). If \( \phi \) is realizable, the processes satisfy \( \psi_2 \) in all runs where the environment faithfully simulates \( A_{\text{safe}} \) and conforms with condition \( \mathcal{L} \) (i.e., \( Q \) and \( \psi_1 \) are true). Then \( p_1, p_2 \) output a halting state of \( M \) and satisfy \( \varphi \), which by Lemma 1, guarantees that the halting state was reached by a legal sequence of configurations of \( M \). In the reverse direction, if \( M \) halts, then \( \phi \) is realizable by (finite) local strategies which output a finite, legal sequence of configurations of \( M \) and conform with condition \( \text{Cond}_0 \). Hence, we obtain the following theorem.

**Theorem 1.** The realizability of specifications from \( \text{LTL}_\mathcal{O} \) in \( A_0 \) is undecidable.

Similarly as in [10], the above argument can be carried out to any architecture which contains an information fork, by introducing additional safety conditions in \( \varphi \), which require that all processes propagate the inputs of the environment to the two processes constituting the information fork. It has also been shown in [10] that in architectures without information forks, the realizability of every \( \text{LTL} \) specification is decidable. Hence, Theorem 1 together with the results from [10] lead to the following corollary.

**Corollary 1.** For every architecture \( A \), the realizability of specifications from \( \text{LTL}_\mathcal{O} \) in \( A \) is decidable if \( A \) does not contain an information fork.

**IV. SYNTHESIS FOR SAFETY SPECIFICATIONS**

In the current section we consider safety specifications where the safety condition consists of propositional formulas.
connected with Boolean operators, and the $\mathcal{X}$ temporal operator. First, we show that the synthesis problem is undecidable for architectures containing an information fork-meet (see Figure 3), by a similar construction as in the case of LTL$_\square$. Then we show that the problem is decidable for the family of star architectures, despite the existence of information forks.

**Fragment LTL$_\square$.** We consider LTL$_\square$ that consists of formulas $\phi$ from the following LTL fragment:

$\psi = P \mid \psi_1 \land \psi_2 \mid \psi_1 \lor \psi_2 \mid \neg \psi \mid X \psi$

$\phi = Q \land \Box \psi$

where $P$, $Q$ are propositional formulas, and $\Box$ is the globally operator. We consider the standard semantics of LTL. The $\Box \psi$ part of $\phi$ specifies a safety condition, and we interpret the $Q$ part as the initial conditions. The fragment LTL$_\square$ can express safety specifications, one of the most basic specification in verification.

While the information fork criterion is decisive for the undecidability of reachability specifications, here we extend this criterion to the family of star architectures of $n + 1$ processes, denoted as $S_n$ (i.e., $p_e$ is the central process, and $\bigcup I(p_i) = O(p_e)$) (Figure 2) and show that: (i) the realizability of some $\phi \in$ LTL$_\square$ in $S_n$ is decidable if all processes receive pairwise disjoint inputs, (ii) it is undecidable if $n \geq 3$ and we allow overlapping inputs. The latter can be generalized to all architectures which contain such a structure, which we call an information fork-meet.

**A. Overlapping inputs**

Here we demonstrate undecidability of realizability of specifications $\phi \in$ LTL$_\square$ for star architectures with overlapping inputs, and with $\phi$ having $\mathcal{X}$-depth 1 (i.e., $\phi$ belongs to a subclass of LTL$_\square$ where $\mathcal{X}$ operators are not nested). We first consider the star architecture $A_1$ (Figure 3), and obtain the undecidability of realizability of such specifications via a reduction from the (non) halting problem.

Given a Turing machine $M$, recall the specification $\varphi$ (from Section 3 for LTL$_\square$) encoding conditions $\mathcal{L}$ and $\text{Cond}_0 - \text{Cond}_4$ through the safety automaton $A_{\text{safe}}$. In contrast with the previous section, here we require that process $p_3$ (instead of $p_e$) faithfully simulates the safety automaton $A_{\text{safe}}$ using the output variables $q_1, \ldots, q_m \in E(p_3, p_e)$. Note that $A_{\text{safe}}$ operates on the variables $x_1, x_2, y_1, y_2$, while $p_3$ does not have access to $y_1$ and $y_2$. However, it can infer these values by simulating $p_1$ and $p_2$ internally, since $p_3$ receives both $x_1$ and $x_2$ (overlapping inputs).

![Fig. 2: The family of start architectures $S_n$.](image)

**Fig. 3: The architecture $A_1$ consists an information fork-meet.**

**Formal proof.** We provide a high level description of the following formulas:

$Q$ specifies that the first state of $A_{\text{safe}}$ according to the output variables $\{q_1, \ldots, q_m\}$ is compatible with the initial values of $x_1, x_2, y_1$ and $y_2$ (i.e., $\{q_1, \ldots, q_m\}$ represent the state of $A_{\text{safe}}$ reached from the initial state after reading the initial values of $x_1, x_2, y_1$ and $y_2$; $Q$ is propositional)

$\psi_1$ specifies that $A_{\text{safe}}$ has a transition from the current state to the next state, encoded by the values of $\{q_1, \ldots, q_m\}$ in the current and the next round, according to the value of variables $x_1, x_2, y_1$ and $y_2$ in the next round (i.e., $p_e$ simulates $A_{\text{safe}}$ faithfully; $\psi_1$ contains only propositional and non-nested $\mathcal{X}$ operators).

$\psi_2$ specifies that $p_1$ and $p_2$ do not output a halting state of $M$ (i.e., $M$ does not terminate; $\psi_2$ is propositional).

$\psi_3$ specifies that $A_{\text{safe}}$ does not reach a rejecting state (i.e., the processes conform to conditions $\text{Cond}_0$-$\text{Cond}_4$ or the environment violates $\mathcal{L}$; $\psi_3$ is propositional).

We construct $\varphi = Q \land \Box (\psi_1 \land \psi_2 \land \psi_3)$. Similarly as in the case of LTL$_\square$, if $\varphi$ is realizable, $p_3$ faithfully simulates $A_{\text{safe}}$ ($Q$ and $\psi_1$ are true), and $p_1, p_2$ output a legal sequence of configurations of $M$, and $\psi_2$ guarantees that $M$ does not halt. In the inverse direction, if $M$ does not halt, then $\varphi$ is realizable by local strategies for which (i) $p_1, p_2$ output a legal sequence of configurations of $M$ and conform with condition $\text{Cond}_0$, and (ii) $p_3$ faithfully simulates $A_{\text{safe}}$. Hence we have the following result.

**Theorem 2.** The realizability of specifications from LTL$_\square$ in $A_1$ is undecidable.

**Remark 1.** We remark that our proof of undecidability in Theorem 2 makes use of infinite-memory strategies, since the processes $p_1$ and $p_2$ are required to output an infinite, non-halting computation. However, we show that even if we restrict the problem to finite-memory strategies, the realizability problem for LTL$_\square$ in $A_1$ still remains undecidable.

**Undecidability for finite-memory strategies.** We define the looping problem as follows. Given a deterministic Turning Machine $M$, decide whether $M$, started on the empty tape, uses only finite memory and does not terminate. The looping problem is undecidable. Indeed, consider the following reduction from the halting problem to the looping problem. For a Turing Machine $N$, we construct Turing Machine $\hat{N}$ that simulates $N$ and counts the number of steps taken by $N$. This
counter is stored on the tape of \( \hat{N} \). If \( N \) halts, \( \hat{N} \) loops at the last configuration and does not increase the counter. Clearly, \( N \) started on the empty tape halts iff \( \hat{N} \) uses only finite memory and does not terminate.

Observe that the reduction in Theorem 2 restricted to finite-memory strategies yields a reduction from the looping problem to the finite-memory realizability problem of LTL\(\square\) in the architecture \( A_1 \).

Indeed, recall that for a (deterministic) Turing Machine \( M \), there are strategies \( \sigma_1, \sigma_2 \) that realize the specification \( \phi \) iff \( M \) does not halt. Let \( C_0, C_1, \ldots \) be a sequence of configurations of \( M \) started at the empty input. Observe that \( M \) loops started on the empty tape iff \( C_0, C_1, \ldots \) are of bounded length. It remains to be shown that \( C_0, C_1, \ldots \) are of bounded length iff \( \sigma_1, \sigma_2 \) are finite memory.

Assume that \( C_0, C_1, \ldots \) are of bounded length. Since \( M \) is deterministic, the infinite sequence of configurations is of the form \( C_1 \ldots \ldots C_k (C_{k-1} \ldots C_m)^n \), for some \( k, m \) with \( k < m \). It follows that there are finite-memory strategies that realize the specification \( \phi \). Conversely, assume that \( \sigma_1, \sigma_2 \) are finite-memory strategies realizing the specification \( \phi \). Consider a run where the environment gives only next signals to the process \( p_1 \). Since the strategies are deterministic, the length of configuration is bounded by the size of the memory of \( \sigma_1 \) and \( \sigma_2 \), or it is infinite because a strategy loops. As \( \sigma_1, \sigma_2 \) output infinitely many configurations, all the configurations \( C_0, C_1, \ldots \) are finite, and thus they are of length bounded by the memory of \( \sigma_1 \) and \( \sigma_2 \).

**Information fork-meet.** We say that an architecture \( A = (P, p_e, V, E) \) has an information fork-meet if there are three processes \( p_1, p_2, p_3 \in P \setminus \{p_e\} \) and paths \( \pi_1, \pi_2 \) in the underlying graph such that

1. the first edges in \( \pi_1, \pi_2 \) are labeled by output variables of \( p_e \),
2. the last edge of \( \pi_1 \) is an input variable of \( p_1 \), but not \( p_2 \),
3. the last edge of \( \pi_2 \) is an input variable of \( p_2 \), but not \( p_1 \),
4. the last edges of \( \pi_1, \pi_2 \) are input variables of \( p_3 \).

Observe that an information fork-meet is a special case of information fork, with a third process that collects all information that is divided between \( p_1 \) and \( p_2 \).

As in the case of LTL\(\Diamond \), the undecidedability argument can be carried to any architecture containing such a structure, by introducing additional conditions in \( \varphi \) which require the rest of the processes to propagate the inputs of the environment to \( p_1, p_2 \) and \( p_3 \) accordingly.

**Corollary 2.** The realizability of specifications from LTL\(\square\) in architectures containing an information fork-meet is undecidable.

### B. Pairwise disjoint inputs

In this subsection we discuss synthesis for formulas \( \phi \in \text{LTL}\(\square\) \) for the class of star architectures, with the additional property that all pairs of processes receive disjoint inputs (i.e., \( \forall i \neq j : I(p_i) \cap I(p_j) = \emptyset \)), denoted as \( S_n \). Our goal is to prove decidability of realizability of such \( \phi \in \text{LTL}\(\square\) \) in every architecture \( A \in S_n \), by showing that whenever such \( \phi \) is realizable, it admits strategies of bounded memory.

Consider some architecture \( A \in S_n \) and an arbitrary \( \phi = \Box \psi \in \text{LTL}\(\square\) \), with the nesting level of \( \chi \) operators in \( \psi \) being \( k \). Assume that \( \phi \) is realizable in \( A \) by local strategies \( \sigma_i \) for every process \( p_i \). These strategies can be represented by \( (p_i) \)-labeled \( I(p_i) \)-trees \( T_{\sigma_i} \). We will show how to construct strategies \( \tau_i \) that also realize \( \phi \), where each tree \( I(p_i) \)-tree \( T_{\tau_i} \) representing \( \tau_i \) is defined from first \( 2^{2^k} |V| + 1 \) levels of \( T_{\sigma_i} \), by applying a folding function given below. We first define the notion of some \( i \in N \) closing \( \neg \psi \).

**Definition 1.** For a computation \( \ell(\pi) \) and some \( i \in N \) we say that \( i \) closes \( \neg \psi \) in \( \ell(\pi) \) if \( \ell(\pi)[i \cdot k, \infty] \models \neg \psi \).

**Remark 2.** \( \ell(\pi) \models \phi \) iff no \( i \) closes \( \neg \psi \) in \( \ell(\pi) \).

Let \( \sigma_1, \ldots, \sigma_n \) be local strategies and \( \sigma \) be the collective strategy induced by \( \sigma_1, \ldots, \sigma_n \). For every \( i \in \{1, \ldots, n\} \), the local strategy \( \sigma_i \) is represented by an \( (p_i) \)-labeled \( I(p_i) \)-tree \( T_{\sigma_i} \). For every node \( x \in T_{\sigma_i} \) with \( |x| \geq k \), we denote with \( x_{\pi} = (x_k, x_{k-1}, \ldots, x_1) \) the \( k \)-node suffix of the unique path to \( x = x_1 \), and define the type of \( x \) under \( \sigma_i \) as \( t_{\sigma_i}(x) = \ell_{\sigma_i}(x_{\pi}) \).

For every level \( l \geq k \) we define the type of \( l \) under \( \sigma \) as \( t_{\sigma}(l) = \{ t_{\sigma_i}(x) : i \in \{1, \ldots, n\}, x \in T_{\sigma_i}, \text{ and } |x| = l \} \), i.e., the type of a level \( l \) is the set of the types of the nodes of level \( l \) of every \( T_{\sigma_i} \), where \( i \in \{1, \ldots, n\} \). Note that there exist at most \( 2^{2^k|V|} \) distinct types of nodes. Consequently, there exist at most \( 2^{2^k|V|} \) distinct types of levels.

Consider some computation \( \ell_{\sigma}(\pi) \) in \( T_{\sigma} \). Observe that whether some \( i \) closes \( \neg \psi \) in \( \pi \) depends only on the \( \ell_{\sigma}(\pi)[i] \) i.e., the type \( t_{\sigma}(\pi[i]) \) determines whether \( i \) closes \( \neg \psi \) in \( \pi \). Hence, we have the following remark:

**Remark 3.** For a formula \( \phi \in \text{LTL}\(\square\) \) there exists a set of types \( \Delta \) such that for every tree \( T_{\sigma} \), a path \( \pi \) in \( T_{\sigma} \) satisfies \( \phi \) if for every tree \( T_{\sigma} \), we have \( t_{\sigma}(\pi[i]) \in \Delta \), i.e., the set of types of nodes in \( T_{\sigma} \) is a subset of \( \Delta \).

**Folding function.** Assume that there exist two levels \( l_1 < l_2 \) such that \( t_{\sigma}(l_1) = t_{\sigma}(l_2) \). Then for every tree \( T_{\sigma} \), for every node \( x \) in level \( l_2 \) there exists a node \( y \) in level \( l_1 \) such that \( t_{\sigma}(x) = t_{\sigma}(y) \), i.e., \( x \) and \( y \) have the same type. For such \( l_1, l_2 \), and every process \( p_i \), we define the folding function \( f_i : (2^{|I(p_i)|})^* \rightarrow (2^{|I(p_i)|})^* \) recursively as follows:

\[
f_i(x) = \begin{cases} x & \text{if } |x| < l_2 \\ y & \text{if } |x| = l_2 \text{ where } |y| = l_1 \text{ and } t_{\sigma}(x) = t_{\sigma}(y) \\ f_i(f_i(y)z) & \text{if } |x| > l_2 \text{ for } x = yz \text{ with } z \in 2^{|I(p_i)|} \end{cases}
\]

and construct local strategies \( \tau_i(x) = \sigma_i(f_i(x)) \). Hence, every strategy \( \tau_i \) behaves as \( \sigma_i \) up to level \( l_2 \), while for nodes further below, it maps them to nodes between levels \( l_1 \) and \( l_2 \), by recursively folding the levels \( l_1 \) and \( l_2 \) with respect to the types of their nodes.

The strategies \( \tau_i \) have the property that they preserve the types of all local nodes up to level \( l_2 \), and only those. Because of the pairwise disjoint inputs, this property is implied for the global nodes of the collective strategy \( \tau \) as well. Observe that the set of all such types serves as the set \( \Delta \) of Remark 3,
which in turn guarantees that the collective strategy $\tau$ also realizes $\phi$. We formalize these arguments below.

The following lemma establishes that for all nodes $x$ in all $T_{\sigma_i}$, the type of $x$ is the same as the type of its image under $f_i$ in the corresponding $T_{\sigma_i}$.

**Lemma 2.** For every $x \in (2^{f_i(p_i)})^+$ with $|x| \geq k$, we have that $t_{\tau_i}(x) = t_{\sigma_i}(f_i(x))$.

**Proof:** Our proof proceeds by induction on $|x|$:  
1) $|x| < l_2$: For all nodes $w$ in $\pi_x$, we have that $\tau_i(w) = \sigma_i(f_i(w)) = \sigma_i(w)$, hence $t_{\tau_i}(\pi_x) = t_{\sigma_i}(\pi_x)$ and thus $t_{\tau_i}(x) = t_{\sigma_i}(f_i(x))$. 
2) $|x| = l_2$: The statement holds by definition. 
3) $|x| = m + 1$: Let $x = yz$ with $|y| = m$. By the inductive hypothesis, $t_{\tau_i}(y) = t_{\sigma_i}(f_i(y))$. We distinguish between the following cases, depending on whether $f_i(y)$ extended by $z$ hits the level $l_3$ (Figure 4):
   (i) $|f_i(y)| < l_2 - 1$: Then $f_i(x) = f_i(f_i(y)z) = f_i(y)z$, that is, if we reach node $x$ by extending node $y$ by an edge $z$, the same holds for their corresponding images under $f_i$. Then $\tau_i(x) = \sigma_i(f_i(x)) = \sigma_i(f_i(y)z)$, thus $t_{\tau_i}(x) = t_{\sigma_i}(f_i(y)z) = t_{\sigma_i}(f_i(x))$ (i.e., the strategy $\tau_i$ will label $x$ as $\sigma_i$ labels its image $f_i(x)$, thus the types of these two nodes are equal). 
   (ii) $|f_i(y)| = l_2 - 1$: By construction, $t_{\sigma_i}(f_i(x)) = t_{\sigma_i}(f_i(y)z)$ (i.e., $f_i(y)$ extended by $z$ hits level $l_2$, and the folding function $f_i$ will bring $x$ to level $l_1$, to a node of the same type). Then $\tau_i(x) = \sigma_i(f_i(x)) = \sigma_i(f_i(y)z)$, hence as in (i), $t_{\tau_i}(x) = t_{\sigma_i}(f_i(y)z) = t_{\sigma_i}(x)$. The desired result follows. 

It follows from the above remark and Lemma 2, that for every $x \in T_{\sigma}$ we have that $t_{\tau_i}(x) = t_{\sigma}(f_i(x))$, where $f(x) = \bigcup_i f_i(x_i)$. That is, the local folding functions $f_i$ result in a unique, global folding function $f$, and the types in the corresponding collective strategy tree are preserved between the global nodes, and their images under $f$. This implies that the set of types occurring in $T_{\sigma}$ is a subset of types of $T_{\sigma}$. Then, by Remark 3 we conclude:

**Lemma 3.** The collective strategy $\tau$ implements $\phi$.

Hence, whenever for a realizable $\phi \in \text{LTL}_{\square}$ exist levels $l_1$ and $l_2$ with the same type under $\sigma$, we can construct a collective strategy $\tau$ for which every local strategy $\tau_i$ uses only the first $l_2$ levels of the corresponding $\sigma_i$, and Lemma 3 guarantees that $\tau$ implements $\phi$. By our previous observation and the pidgeonhole principle, $l_2$ is upper bounded by $2^{|\text{LTL}_{\square}|} + 1$, and thus every local strategy $\tau_i$ operates in the first $2^{|\text{LTL}_{\square}|} + 1$ levels of the corresponding $T_{(p_i)}$-tree. There are a bounded number of local strategies with this property, thus the problem of realizability in this case reduces to exhaustively exploring all of them. Moreover, it follows from our analysis that local nodes in the same level and having the same type can be merged, since the local strategy that behaves identically in both subtrees preserves the set of types appearing in the global tree. Hence, the width of each level is bounded by the number of different possible types, $2^{|\text{LTL}_{\square}|}$.

**Theorem 3.** The realizability of $\phi \in \text{LTL}_{\square}$ for the class $\mathcal{S}_n$ of star architectures with pairwise disjoint inputs is decidable in EXPSPACE.

**Proof:** We describe a non-deterministic Turing machine $N$ which, given an architecture $A \in \mathcal{S}_n$ and a specification $\phi \in \text{LTL}_{\square}$ as input, decides whether there exist local strategies $\sigma_i$ which realize $\phi$. The machine $N$ operates in levels, as follows: It first guesses the top $k$ levels of each strategy $\sigma_i$, it verifies that $\phi$ has not been violated, and it computes the set $\Delta_i$ of all types of nodes in the $k$-th level of each $\sigma_i$. Then, iteratively for each level $j$ up to $2^{|\text{LTL}_{\square}|} + 1$, it guesses the sets $\Delta^j_i$ of types of nodes of the new level of each $\sigma_i$, such that $\Delta^j_i$ is compatible with $\Delta^{j-1}_i$, and verifies that all possible combinations of types between all $\Delta^j_i$ do not violate $\phi$.

It is straightforward to verify that if $N$ accepts, $\phi$ is realizable by local strategies $\sigma_i$, such that for every $x \in T_{\sigma_i}$, the strategy $\sigma_i$ assigns to $x$ the value of a type in $\Delta^{|x|}_i$ which is compatible with $x$. Conversely, if there are local strategies $\sigma_i$ that realize $\phi$, the machine $N$ can guess their levels, and thus accept. Therefore, if $N$ rejects, no such strategies exist. Finally, $N$ operates in $\text{NEXSPACE}$, since $|\Delta^j_i| \leq 2^{|\text{LTL}_{\square}|}$, and the first $k$ levels of each $\sigma_i$ has less than $2^{|\text{LTL}_{\square}|}$ nodes. By Savitch’s Theorem [19], $\text{NEXSPACE} = \text{EXPSPACE}$. 

**V. SYNTHESIS WITHOUT THE NEXT OPERATOR**

In the current section we consider a fragment of LTL without the $\chi$ operator, for which the problem of realizability is decidable in non-deterministic exponential time in the size of the specification.
Fragment LTL\(_{\text{AG}}\). We consider LTL\(_{\text{AG}}\) that consists of formulas \(\phi\) from the following LTL fragment:

\[
\phi = \bigwedge_i \Box P_i \rightarrow \left( \bigwedge_i \Box Q_i \land \bigwedge_i \Box R_i \land \bigwedge_i \Box F_i \right)
\]

\[
= \Box \bigwedge_i P_i \rightarrow \left( \bigwedge_i \Box Q_i \land \bigwedge_i \Box R_i \land \bigwedge_i \Box F_i \right)
\]

\[
= \Box P \rightarrow \left( \Box Q \land \Box R \land \bigwedge_i \Box F_i \right)
\]

for \(i \in \{1, \ldots, m\}\), with \(P_i, Q_i, R_i, F_i\) propositional formulas, and \(P = \bigwedge_i P_i, Q = \bigwedge_i Q_i\). We consider the standard semantics of LTL. We first observe that every realizable safety formula \(\psi = \Box Q\), where \(Q\) is propositional, admits memoryless strategies. The LTL\(_{\text{AG}}\) can express specification that consists of conjunction of safety assumptions, and guarantees where each guarantee is a safety, reachability, or a liveness condition.

**Lemma 4.** Let \(A\) be any architecture. Every formula \(\psi = \Box Q\), for some propositional \(Q\), is realizable in \(A\) iff it is realizable by memoryless strategies.

**Proof:** The right to left direction is immediate. For the left to right direction, assume that \(\psi\) is realizable, and let \(\sigma_i\) be the strategy of process \(p_i \in P\) in \(A\). Construct strategies \(\tau_i\) such that \(\tau_i(x) = \sigma_i(z)\) for every \(x = zy \in (2^{I(p_i)})^z \) and \(z \in 2^{I(p_i)}\), and let \(\tau\) be the collective strategy of \(\tau_i\). For every infinite path in \(T\), we have that \(\ell_{\tau}(\pi)[i] = \ell_{\sigma}(\pi)[1]\) for all \(i \in N\). Since \(\sigma\) implements \(\psi\), it follows that \(\ell_{\tau}(\pi)[1] \equiv Q\). Hence for all \(i\), we have \(\ell_{\tau}(\pi)[i] \equiv Q\), and thus \(\ell_{\tau}(\pi) \equiv \psi\), and all \(t_i\) are memoryless strategies.

The following lemma establishes that reachability and safety specifications of propositional formulas are equivalent with respect to realizability.

**Lemma 5.** Let \(A\) be any architecture. For every formula \(\psi = \Box Q\) for some propositional \(Q\), \(\psi\) is realizable in \(A\) iff \(\psi' = \Box Q\) is realizable in \(A\).

**Proof:** The left to right direction is immediate. For the right to left direction, assume that \(\psi\) exists local strategies \(\sigma_i\) such that the collective strategy \(\sigma\) implements \(\psi\), and let \(T_\sigma\) be the corresponding collective strategy tree. Then there exists some node \(x \in T_\sigma\) such that for all \(z \in 2^{O(p_i)}\), we have \(\ell(z) \equiv Q\), otherwise we can construct an infinite path \(\pi\) such that \(\pi[i] \not\equiv Q\) for all \(i \in N\) (and thus \(\ell(\pi) \not\equiv \psi\)), by choosing for every \(x \in T_\sigma\) a \(z \in 2^{O(p_i)}\) such that \(\ell(z) \not\equiv Q\). It follows that for local strategies \(\tau_i\) such that \(\tau_i(yz) = \sigma_i(xz)\) for all \(y, z\), the collective strategy \(\tau\) implements \(\psi\).

**Lemma 6.** Let \(A\) be any architecture. Every formula \(\phi\) of LTL\(_{\text{AG}}\) is realizable in \(A\) iff every \(\phi_{R_i} = \Box (P \rightarrow (Q \land R_i))\) and every \(\phi_{F_i} = \Box (P \rightarrow (Q \land F_i))\) is realizable in \(A\).

**Proof:** (i) For the right to left direction, assume that there exist families of memoryless (by Lemma 4) local strategies \((\sigma_j^{R_i})\) and \((\sigma_j^{F_i})\) for every process \(p_j\), such that the collective strategy \(\sigma_j^{R_i}\) implements \(\phi_{R_i}\), and the collective strategy \(\sigma_j^{F_i}\) implements \(\phi_{F_i}\). Construct local strategies \(\tau_j\) such that for every \(x = yz\) with \(|z| = (1 + |x|) \bmod 2m\), we have \(\tau_j(x) = \sigma_j^{R_i}(z)\) if \(|z| \leq m\), and \(\tau_j(x) = \sigma_j^{F_i-n}(z)\) if \(|z| > m\) (i.e., the local strategy \(\tau_j\) repeatedly alternates between all the strategies \(\sigma_j^{R_i}\) in the first \(m\) steps, and between all the strategies \(\sigma_j^{F_i}\) the next \(m\) steps). Let \(\tau\) be the collective strategy of all \(\tau_j\) and consider an arbitrary path \(\pi\) in \(T\). Either \(\ell_{\tau}(\pi)[k] \equiv \neg P\) for some \(k\), or for all \(k\), it holds \(\ell_{\tau}(\pi)[k] \equiv P\), and by construction, for \(i = 1 + k \bmod 2m\), we have \(\ell_{\tau}(\pi)[k] \equiv Q \land R_i\) when \(i \leq m\) and \(\ell_{\tau}(\pi)[k] \equiv Q \land R_{i-m}\) when \(i > m\). In both cases, \(\ell_{\tau}(\pi) \equiv \phi\).

(ii) For the left to right direction, assume that for some \(i\), \(\phi_{R_i}\) is not realizable (the analysis is similar for \(\phi_{F_i}\)). By Lemma 5, \(\Box (P \rightarrow (Q \land R_i))\) is not realizable. Hence, for any collective strategy \(\sigma\) there exists some path \(\pi\) in \(T_\sigma\), such that for all \(k\), we have \(\ell_\sigma(\pi)[k] \equiv P \land (\neg Q \lor \neg R_i)\), and \(\sigma\) does not implement \(\phi\).

Hence, Lemma 6 establishes that every formula \(\phi \in \text{LTL}_{\text{AG}}\) is realizable if and only if it admits local strategies for all the corresponding \(\phi_i\), by providing a constructive argument. As a consequence of Lemma 4, deciding whether every \(\phi_i\) is realizable reduces to realizing the propositional formula \((P \rightarrow (Q \land R_i \land F_i))\). This can be done in NEXPTIME, by having a non-deterministic Turing machine guessing the local strategies of all processes, and verifying that such strategies satisfy the formula under all the (exponentially many) possible inputs of the environment. We show that the problem is also NEXPTIME-hard, via a reduction from the Dependency Quantifier Boolean Formula (DQBF) validity problem introduced in [20] to study time bounded multiplayer alternating machines. A DQBF is a quantified Boolean formula with a succinct description of dependencies between the quantified variables. Every DQBF has an equivalent form in which all existentially quantified variables are substituted by existentially quantified Skolem functions defined over their dependencies, and appearing at the beginning of the formula (e.g., \(\forall x_1 \forall x_2 \exists y_1(x_1) \exists y_2(x_2) \varphi(x_1, x_2, y_1, y_2)\) is a DQBF stating that \(y_i\) depends on \(x_i\), and has a functional form \(\exists x_1 \exists x_2 \varphi(x_1, x_2, y_1, y_2)\) with \(\varphi\) the Skolem functions).

**Lemma 7.** Given an architecture \(A\) and a formula \(\phi \in \text{LTL}_{\text{AG}},\) deciding whether \(\phi\) is realizable in \(A\) is NEXPTIME-hard.

**Proof:** Consider any DQBF formula \(\psi : \forall x_1 \ldots \forall x_k \exists y_1(x_1) \ldots \exists y_n(x_n) \varphi(x_1, x_2, y_1 \ldots y_n)\) with \(k\) universally quantified variables \(x_i\) and \(n\) existentially quantified variables \(y_i\). We assume w.l.o.g. that the dependencies of each \(y_i\) are on only some universally quantified variables \(x_i\). We construct the architecture \(A = (P, p_i, V, E)\), where \(P\) contains \(n + 1\) processes, \(V = \{x_i \in \psi\} \cup \{y_i \in \psi\}\), process \(p_i\) receives as inputs from the environment all \(x_i\), outputs variable \(y_i\), while the environment uses all remaining \(x_j\) as hidden variables. We construct the specification \(\phi = \Box \psi \in \text{LTL}_{\text{AG}}\). Both \(A\) and \(\phi\) are polynomial in the size of \(\psi\). Because of Lemma 4,
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VI. Conclusions

In this paper we studied the distributed synthesis problem for relevant fragments of LTL. We presented a much finer characterization of undecidability results for distributed synthesis in terms of LTL fragments that uses eventually, globally and next operators. In contrast to previous decidability results that were non-elementary, we identify fragments where the complexity is EXPSPACE (or NEXPTIME-complete). An interesting direction of future work would be to develop efficient algorithms for the problems for which we establish decidability, obtain efficient implementations of the algorithms for distributed synthesis problems, and finally consider some case-studies of practical examples.

References